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## U.S. PATENT DOCUMENTS

## FOREIGN PATENT DOCUMENTS

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

	HF	IBM Technical Disclosure Bulletin, "Sub-Block Cache with Demand Sub-Block Reloading in Conjunction with A Risc System", Vol. 35, No. 1A, June 1992, pp. 84-85.
	HF	INSPEC, Yonghwan, L. et al., 1997-1998.

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	HF	Barrera, "Sub-Block Cache with Demand Sub-Block Reloading in Conjunction with a RISC System", Technical Disclosure Bulletin, No. 1A, June 1992, pp. 84-85.
	HF	Jeffrey C. Mogul, "Clarifying the Fundamentals of HTTP", May 2002, pp. 25-36.

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	HF	Kelly et al., "Aliasing on the World Wide Web: Prevalence and Performance Implications", May 2002, pp. 281-292.
	HF	IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences, Lee, Y. et al., "Unified Tag Memory Architecture with Snoop Support", Vol. E81-A, Issue 6, June 1998, pp. 1172-75.

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	HF	International Semiconductor Conference, 20th Edition, October 7-11, 1997, Sinaia, Romania, CAS '97 Proceedings, Vol. 1, Lee, Y. et al. "Shared Tag for MMU and Cache Memory", pp. 77-80.
	HF	Electronics Letters, Vol. 33, Issue 21, October 9, 1997, Lee, Y. et al., "Indirectly-Compared Cache Tag Memory Using a Shared Tag in a TLB", pp. 1764-1766.

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